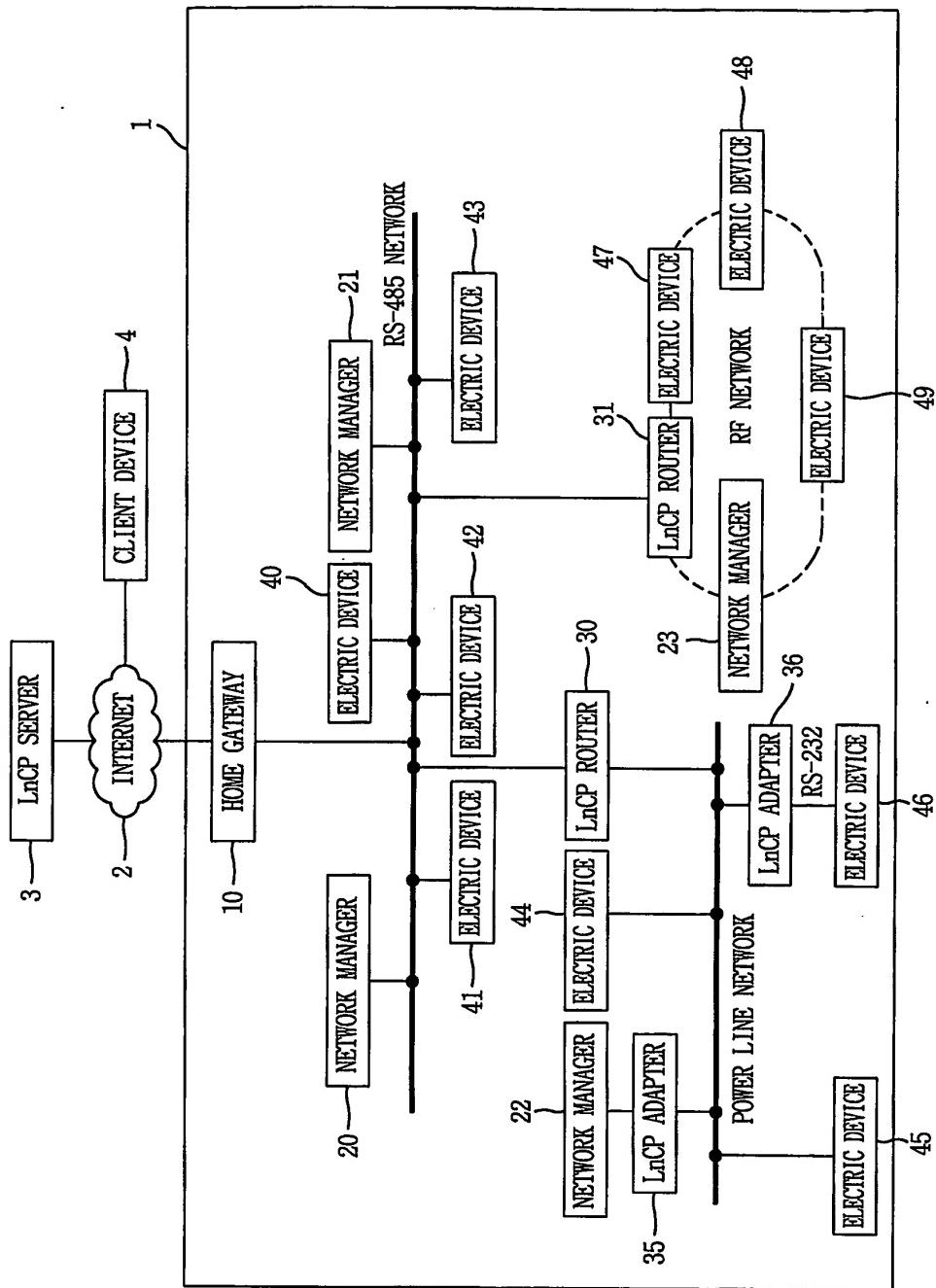


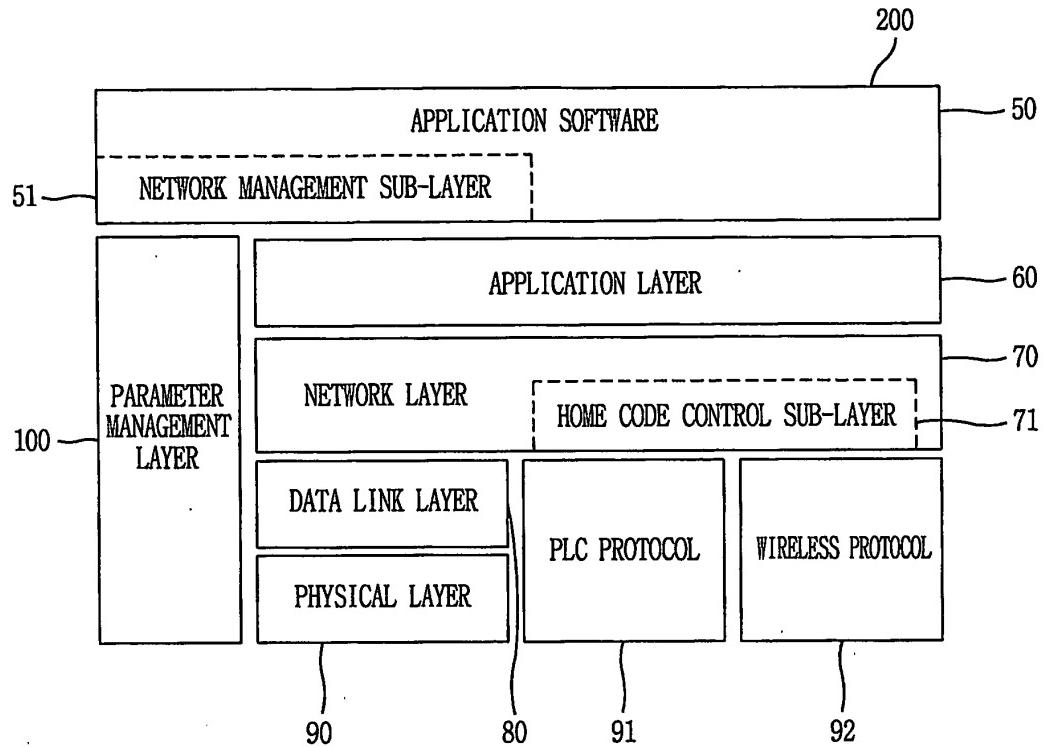
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FIG. 1



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FIG.2



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FIG.3A

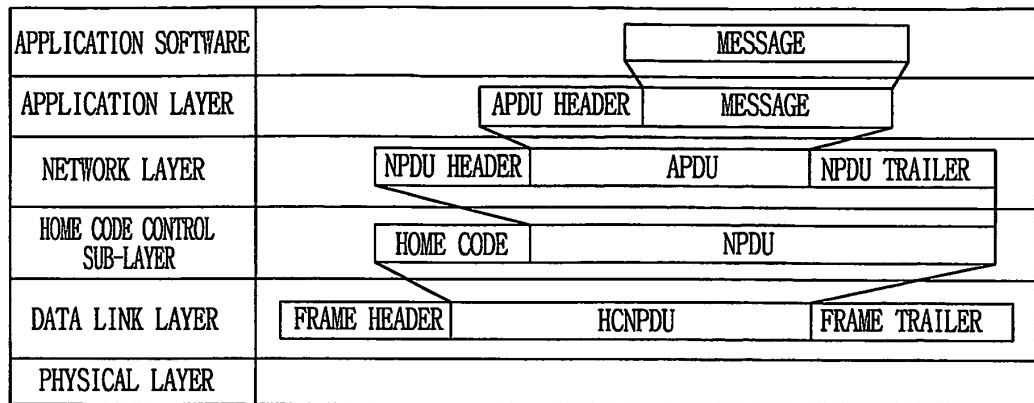
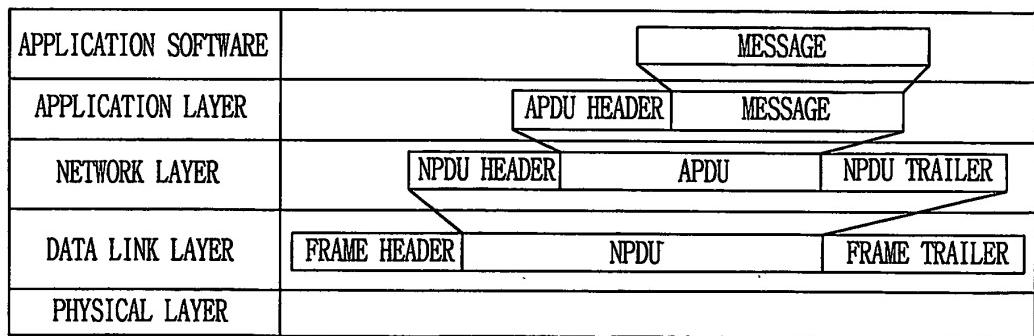


FIG.3B



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FIG.4A

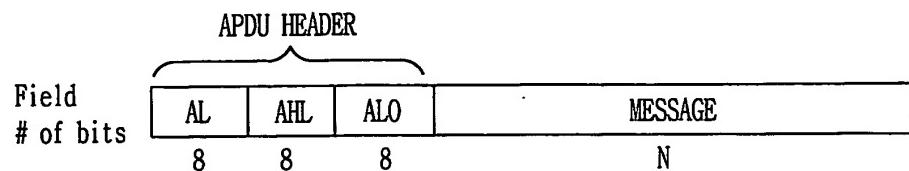


FIG.4B

Field # of bits	SLP	DA	SA	PL	NLC	APDU		CRC	ELP
	8	16	16	8	24	N		16	8

FIG.4C

Field # of bits	SP	NHL	PV	NPT	TC	PN
	3	5	8	4	2	2

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FIG.4D

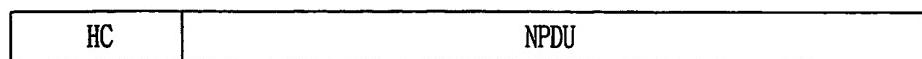


FIG.4E



FIG.4F

